

Welcome to the DesignAdvance eNewsletter

Leading technology companies recognize that reductions in PCB design time have a direct impact on time-to-market for new products, which directly correlates to profitability. Since the initial release of CircuitSpace, we have worked closely with our customers in developing next generation intelligent EDA solutions for PCB design.

The information in this newsletter is intended to provide you with with an overview of the world of DesignAdvance and more importantly, information relating to your continued success in PCB Design.

- Ed Pupa, President and CEO

Newsletter Contents:

- Product Spotlight
- Upcoming Web Seminars
- In the News
- Free ROI Calculator

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UPCOMING WEB SEMINARS

[Enhancing your PCB Development Process](#)

Join us June 16th or July 15th for an advanced overview of how to shorten your PCB development cycle by weeks, improving design efficiency while working seamlessly within your existing design methodology.

This 45 minute web demonstration will highlight key benefits of DesignAdvance products within the PCB design flow.



PRODUCT SPOTLIGHT

Cross-Probing with CircuitSpace

CircuitSpace v2.2 adds a major new enhancement that allows the user to have bi-directional communication between layout and a (PDF) schematic. The new feature gives both Designers and Engineers instant selection and verification of design elements such as components, nets, or pins, within Cadence Allegro and the (PDF) schematic. Through Cross-probing, the user can populate any Cadence Allegro selection set through our proprietary interactive clustering. [\(learn more\)](#)

IN THE NEWS

- [Introducing CircuitProbe](#)
- [DesignAdvance Releases CircuitSpace v2.2 - Unveils New Cross-Probing Technology](#)
- [Back to Basics - DesignAdvance is One of the EDA Industry's Newest and Most Innovative Companies - Clive Maxfield](#)
- [EMA Expands PCB Product Portfolio to Include CircuitSpace from DesignAdvance Systems](#)

FREE ROI Calculator

Our customers have achieved up to 5X returns (2.5 months) on their investment within the first year of using CircuitSpace. Designers reduce board layout and placement time from weeks-to-minutes through CircuitSpace's AutoClusteringŽ, intelligent design (IP) reuse, and Cross-Probing technologies.

Users enter their PCB design information by completing a short form and the calculator automatically generates a summary of the time and cost savings.

How much TIME and MONEY will you save? Click below and see for yourself.

	Go From Your Current Design Flow	To CircuitSpace	You Save
Layout Design Team			
Design Time	<u>19.1</u> Weeks	<u>0.6</u> Weeks	<u>18.5</u> Weeks
Money	<u>\$34,642</u> Dollars	<u>\$1,021</u> Dollars	<u>\$33,621</u> Dollars
Hardware Engineering Team			
Design Time	<u>1.7</u> Weeks	<u>0.3</u> Weeks	<u>1.4</u> Weeks
Money	<u>\$4,413</u> Dollars	<u>\$779</u> Dollars	<u>\$3,635</u> Dollars
Total			
Design Time	<u>20.8</u> Weeks	<u>0.9</u> Weeks	<u>19.9</u> Weeks
Money	<u>\$39,056</u> Dollars	<u>\$1,800</u> Dollars	<u>\$37,256</u> Dollars

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